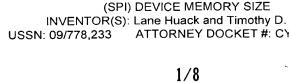
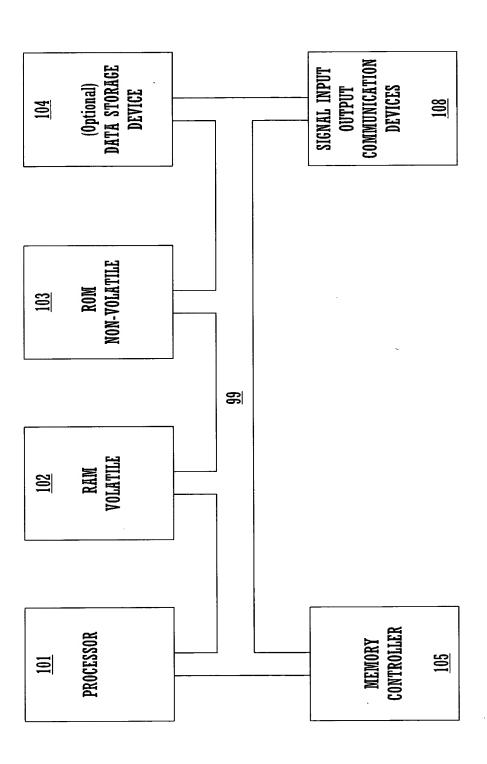
INVENTOR(S): Lane Huack and Timothy D. Jordan ATTORNEY DOCKET #: CYPR-CD00205









INVENTOR(S): Lane Huack and Timothy D. Jordan USSN: 09/778,233 ATTORNEY DOCKET #: CYPR-CD00205



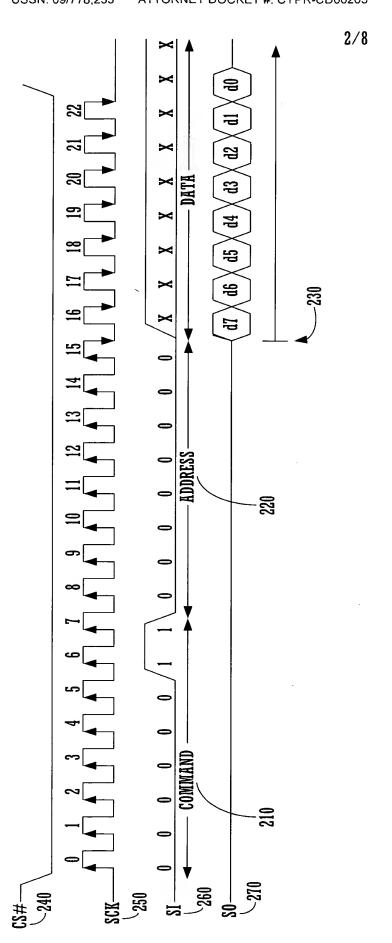
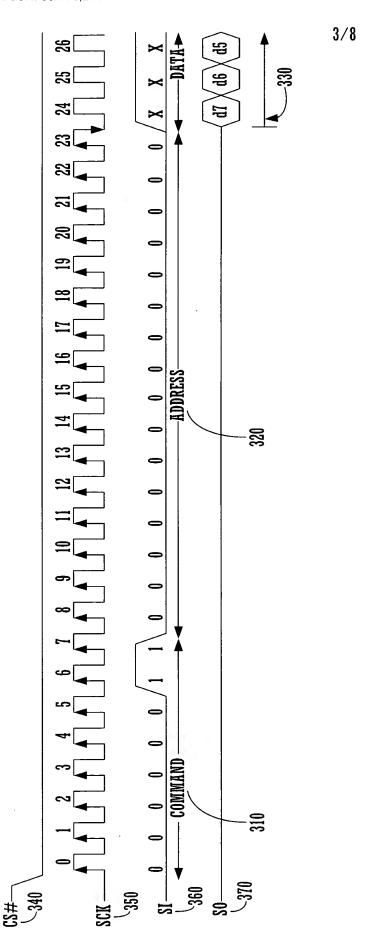


FIGURE 2

INVENTOR(S): Lane Huack and Timothy D. Jordan: 09/778,233 ATTORNEY DOCKET #: CYPR-CD00205 USSN: 09/778,233





TITLE: METHOD AND APPARATUS FOR AUTOMATIC DETECTION OF A SERIAL PERIPHERAL INTERFACE (SPI) DEVICE MEMORY SIZE

INVENTOR(S): Lane Huack and Timothy D. Jordan USSN: 09/778,233 ATTORNEY DOCKET #: CYPR-CD00205

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Replacement Sheet

<u>400</u>

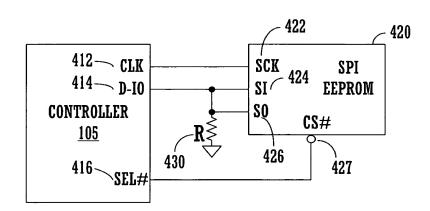
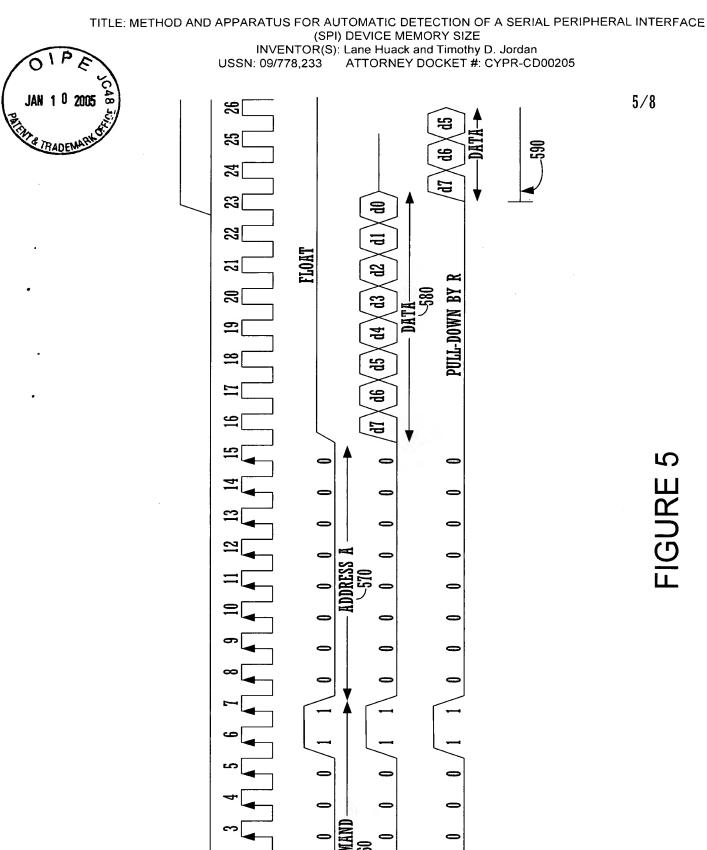


FIGURE 4



(SPI) DEVICE MEMORY SIZE
INVENTOR(S): Lane Huack and Timothy D. Jordan
: 09/778,233 ATTORNEY DOCKET #: CYPR-CD00205 USSN: 09/778,233



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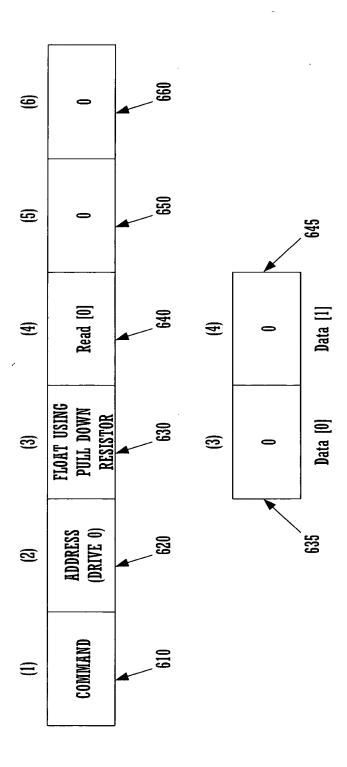


FIGURE 6

JAN 1 0 2005 &

(SPI) DEVICE MEMORY SIZE
INVENTOR(S): Lane Huack and Timothy D. Jordan
USSN: 09/778,233 ATTORNEY DOCKET #: CYPR-CD00205

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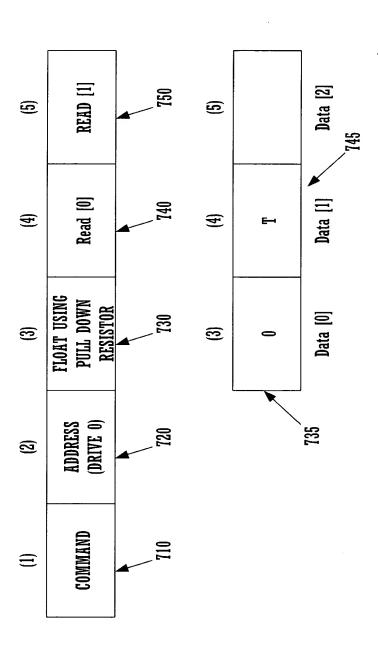


FIGURE 7

TITLE: METHOD AND APPARATUS FOR AUTOMATIC DETECTION OF A SERIAL PERIPHERAL INTERFACE (SPI) DEVICE MEMORY SIZE INVENTOR(S): Lane Huack and Timothy D. Jordan ATTORNEY DOCKET #: CYPR-CD00205 USSN: 09/778,233 JAN 1 0 2005 8/8 800 **START** SEND THE READ COMMAND BYTE. 810 DRIVE THE DATA INPUT/OUTPUT PIN LOW FOR EIGHT SERIAL CLOCK PULSES. 820 FLOAT THE DATA INPUT/OUTPUT PIN. 830 WEAKLY PULL DOWN THE DATA INPUT/OUTPUT PIN TO A LOGIC "O" LEVEL. 840 DETECT THE PRESENCE OF ZERO OR NON-SERO BYTE VALUES AT THE DATA INPUT/OUTPUT PIN OF THE CONTROLLER TO DETERMINE THE PRESENCE OF A SMALL, LARGE, OR LARGER SPI EEPROM, OR ALTERNATIVELY, THE ABSENCE OF AN SPI EEPROM. <u>850</u> **END**

FIGURE 8